

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and ((synthesiz\$3 with frequency) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and ((device with under with test) DUT) and (oscillation with frequency) and (select\$3 with oscillator) and synchroniz\$3).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/13 17:16
L4	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and ((synthesiz\$3 with frequency) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and ((device with under with test) DUT) and (oscillation with frequency) and (select\$3 with oscillator) and synchroniz\$3).clm.	US-PGPUB	OR	ON	2007/03/13 17:16
L5	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and (synthesiz\$3 with frequency with receiv\$3 with ((device with under with test) DUT)) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and (oscillation with frequency with synchroniz\$3) and (select\$3 with oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/13 17:19
L6	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and (synthesiz\$3 with frequency with receiv\$3 with ((device with under with test) DUT)) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and (oscillation with frequency with synchroniz\$3) and (select\$3 with oscillator)	US-PGPUB	OR	ON	2007/03/13 17:19
L7	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and ((synthesiz\$3 with frequency with receiv\$3 with ((device with under with test) DUT)) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and (oscillation with frequency with synchroniz\$3) and (select\$3 with oscillator)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/13 17:20

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L8	1	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and ((synthesiz\$3 with frequency with receiv\$3 with ((device with under with test) DUT)) and (PLL (phase clock with loop)) and (divid\$3 with frequency) and (oscillation with frequency with synchroniz\$3) and (select\$3 with oscillator).clm.	US-PGPUB	OR	ON	2007/03/13 17:20
S3	1	(test\$3 with system) and tester and oscillator and (first with frequency) and (second with frequency) and (load with board) and (error with detector) and (error with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:51
S4	3	(test\$3 with system) and tester and oscillator and (load with board) and (error with detector) and (error with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 15:02
S5	76	tester and oscillator and board and (error with (detector sensor)) and (error with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:29
S6	34	S5 and divider	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 15:03
S7	13	S6 and (device with under with test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:31
S8	4	S7 and (compar\$6 with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 15:04
S9	16	S6 and (phase with lock\$2 with loop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 15:06

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S10	6	S7 and (phase with lock\$2 with loop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:37
S11	0	(tester same oscillator same devider) and board and (error with (detector sensor)) and (error with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 15:10
S12	6081	"324"/\$.ccls. and (test\$3 with integrated with circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:35
S13	396	S12 and (board with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:36
S14	22	S13 and (oscillator with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:36
S15	7	S14 and (phase with lock\$2 with loop)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:38
S16	0	S15 and (data with proxess\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:38
S17	5	S15 and (data with process\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:38

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S18	0	(test\$3 with system) and tester and oscillator and (first with frequency) and (second with frequency) and (load with board) and (error with detector) and (error with signal) and data and process\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/21 16:39
S19	179	tester and oscillator and board and (frequency with control\$4) and synchronization	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:35
S20	26	S19 and (device with under with test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/04/24 18:31
S22	7	tester and oscillator and board and (frequency with control\$4) and (synchroniz\$4 with tester)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:59
S23	507	(synchroniz\$3 with tester)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:36
S24	37	S23 and (divider with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:37
S25	28	S24 and ((phase with lock\$3 with loop) PLL)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:48
S26	14	S25 and ((oscillator oscillation) with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:49

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S27	1	S26 and (load with board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:42
S28	3	S26 and board	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:47
S29	1	S24 and (load with board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:47
S30	1	S24 and (load with board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:47
S31	34	S23 and (load with board)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:48
S32	4	S31 and ((phase with lock\$3 with loop) PLL)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:48
S33	1	S32 and ((oscillator oscillation) with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:49
S34	2	S31 and ((oscillator oscillation) with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:49

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S35	6	tester and ((oscillator oscillation) with frequency) and (load with board) and (error with detector)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:34
S36	2	S35 and (error with signal with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:54
S37	2	S35 and (error with frequency)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:54
S38	3	S35 and (error with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 15:54
S39	2	(tester (probe near2 test\$3)) and ((oscillator oscillation) with frequency) and (load adj board) and (error with detector with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:36
S40	58	(tester (probe near2 test\$3)) and ((oscillator oscillation) with frequency) and ((load adj board) (test\$3 with integrat\$3 with circuit)) and (error with detector with signal)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:46
S41	166	(tester (probe near2 test\$3)) and ((oscillator oscillation) with frequency) and ((load adj board) (test\$3 with integrat\$3 with circuit)) and ((phase with lock\$3 with loop) PLL)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:54
S42	23	S41 and divider and DUT	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:47

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S43	40	(tester (probe near2 test\$3)) and ((oscillator oscillation) with frequency) and ((load adj board) (test\$3 with integrat\$3 with circuit)) and (((phase with lock\$3 with loop) PLL) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:56
S44	4	(tester (probe near2 test\$3)) and ((oscillator oscillation) with frequency) and ((load adj board) (test\$3 with integrat\$3 with circuit)) and (((phase with lock\$3 with loop) PLL) with coupled with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/09/25 16:57
S45	7	S43 and (synchroniz\$4 with tester)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 06:42
S46	1	(synchroniz\$3 with signal) and (test\$3 with system) and (multiplexer with (coupl\$3 relat\$3) with oscillator) and (multiplexer with (coupl\$3 relat\$3) with divider) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 06:56
S47	1	(synchroniz\$3 with signal) and (device with under with test) and (multiplexer with (coupl\$3 relat\$3) with oscillator) and (multiplexer with (coupl\$3 relat\$3) with divider) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 06:56
S48	2	(synchroniz\$3 with signal) and (multiplexer with (coupl\$3 relat\$3) with oscillator) and (multiplexer with (coupl\$3 relat\$3) with divider) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 08:55
S49	1	synchroniz\$3 and test\$3 and (multiplexer with (coupl\$3 relat\$3) with oscillator) and (multiplexer with (coupl\$3 relat\$3) with divider) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 07:03

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S50	4	test\$3 and (multiplexer with (coupl\$3 relat\$3) with oscillator) and (multiplexer with (coupl\$3 relat\$3) with divider) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 07:54
S51	4	test\$3 and (multiplexer with (coupl\$3 relat\$3) with oscillators) and (multiplexer with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 07:57
S52	17	(multiplexer with (coupl\$3 relat\$3) with oscillators) and (multiplexer with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 07:58
S53	3	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (multiplexer with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with plurality with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 08:59
S54	1	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (multiplexer with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) and synchroniz\$3 and test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 08:44
S55	1	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (multiplexer with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) and synchroni\$4 and test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:04
S56	1	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (multiplexer with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) and test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 08:45
S57	6	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (multiplexer with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 08:47

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S58	0	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with plurality with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:00
S59	1	(multiplexer with (coupl\$3 relat\$3) with plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with plurality with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:00
S60	1	(multiplexer with (coupl\$3 relat\$3) with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:01
S61	1	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:01
S62	1	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with DUT)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:03
S63	80	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 09:39
S64	37	S63 and synchroni\$4 and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:30

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S65	80	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3 with dividers) and (((phase with lock\$3 with loop) PLL) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:49
S66	37	S65 and synchroni\$4 and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:31
S67	80	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3 with dividers) and (((phase with lock\$3 with loop) PLL) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:43
S68	37	S67 and synchroni\$4 and test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:43
S69	36	S68 and (select\$3 with oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/25 09:43
S70	35	("702"/\$.ccls. "331"/\$.ccls. "714"/\$.ccls. "324"/\$.ccls.) and multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3 with dividers) and (((phase with lock\$3 with loop) PLL) with dividers)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/13 17:00
S71	23	(synchroniz\$3 with signal) and (((phase with lock\$3 with loop) PLL) with ((device with under with test) DUT))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 09:12
S72	8	S71 and oscillator and divider	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 08:57

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S73	0	(synchroniz\$3 with signal) and (((device with under with test) DUT) with reciev\$3 with ((phase with lock\$3 with loop) PLL)) and (((device with under with test) DUT) with reciev\$3 with divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 09:35
S74	2	"6859028".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 09:35
S75	50	multiplexer and (plurality with oscillators) and (((phase with lock\$3 with loop) PLL) with (coupl\$3 relat\$3) with dividers) and (((phase with lock\$3 with loop) PLL) with dividers) and (crystal near1 oscillator)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/26 09:39